

Highly Selective MEMS Release

XACTIX, Inc

Overview

- Corporate Overview
- Xenon Difluoride Etch Process
- MEMS Application Examples
- Other Application Examples
- XACTIX Equipment

XACTIX Overview



- The source for xenon difluoride etching technology
- Ten years working with premier commercial customers and MEMS R&D programs.
- Over 100 Machines installed or in Progress
- US, Japan, Taiwan, Korea, China, Europe, Canada, Malaysia
- Full time process and equipment R&D effort leading to a growing IP position.
- Sales & marketing and development agreements with STS



Location

Pittsburgh Pennsylvania



Washington D.C. - 4hrs by car
New York City - 6hrs by car
Philadelphia - 5 hrs by car
Boston, Chicago - 1.5 hrs by Airplane
San Francisco - 4.5 hrs by Airplane



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Selected Customers

Commercial

- Analog Devices
- Lucent
- IBM
- Qualcomm
- Seagate
- Corning
- Northrop Grumman
- Hitachi
- Japanese Consumer Companies

Universities and Labs

- US Army Laboratory
- NASA Goddard Space Flight Center
- National Institute of Standards
- Stanford
- Cornell
- Carnegie Mellon University
- University of Michigan
- Penn State University
- ITRI
- Ritsumeikan University
- University of Twente

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Xenon Difluoride

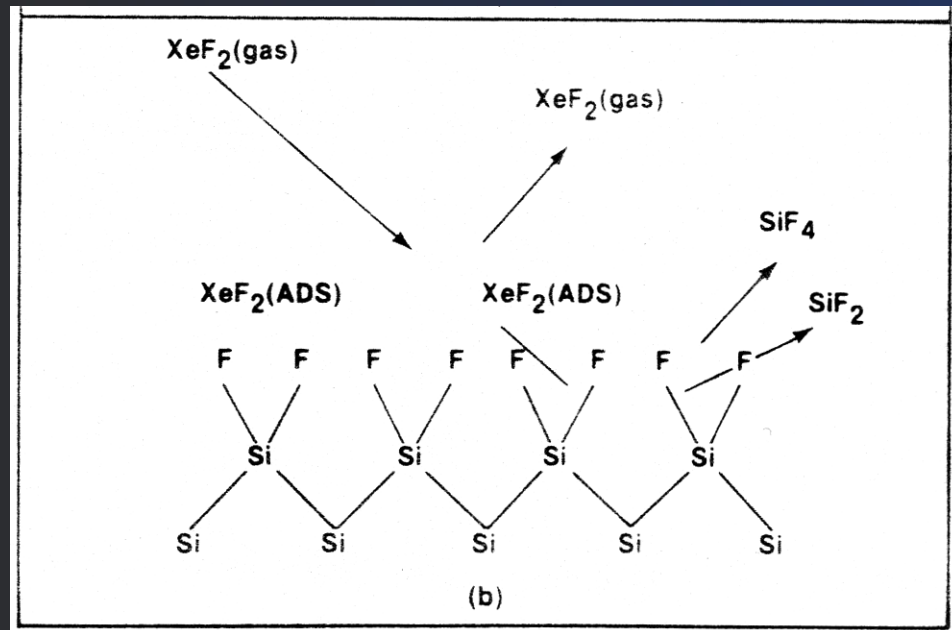
*Isotropic Silicon Etch
For MEMS Release*



- Highly Selective MEMS Release
- No Stiction Problems
- Long Undercuts
- Release Using Nano-scale Holes and Release Layers

Etching Process

- $2\text{XeF}_2 + \text{Si} \rightarrow 2\text{Xe} + \text{SiF}_4$
- Selectively etches silicon, molybdenum and germanium.
- Isotropic etching
- Dry, vacuum based process (NO plasma or other activation needed)
- XeF_2 sublimates from solid crystals to form the vapor-phase etchant

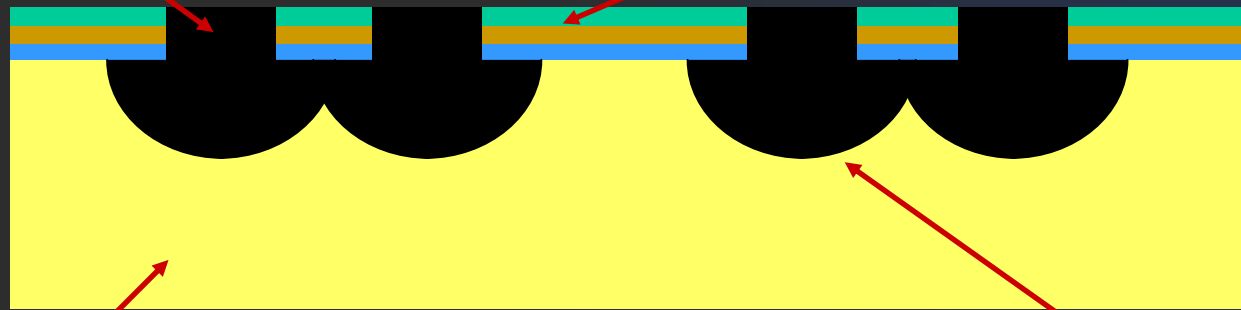


Bulk Silicon Surface Micromachining

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Pattern Device Layers

Deposit Device Layers



Si Substrate

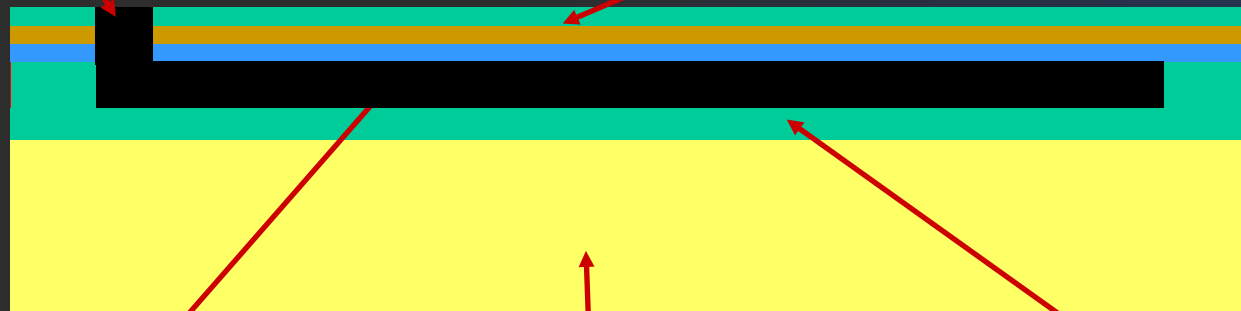
Isotropic Si Etch to Undercut Device

Surface Micromachining



Define Etch Hole

Deposit Device Layers



Deposit and Patterned
Sacrificial Layer

Substrate

Deposit Etch Stop

As Late As Possible Release

- Once MEMS can move they can break
- Release after Capping
 - Avoid physical damage from capping process.
 - Go from release to hermetic seal under vacuum.
- Release at Packaging Fab
 - Avoid physical damage during transit
 - Avoid corrosion or stiction caused by moisture exposure after release.
- Release After Dicing
 - Avoid physical damage from vibration or heat and washing
- Release After Package Insertion and Wire Bonding
 - Avoid physical damage from vibration
 - Avoid exposure to packaging chemicals.

Etch Rates: Loading

- Typical Surface Micromachining Application with Si on 6" wafers: 2 to 3 $\mu\text{m}/\text{min}$
- Small Chips: up to 10 $\mu\text{m}/\text{min}$
- Fully exposed 6" wafer: 0.2 $\mu\text{m}/\text{min}$

Reactive Materials

Material	Selectivity to Si
Si	1:1
Mo	2:1
Ge	Same or Faster than Si
SiGe	Same or Faster than Si

Conditionally Reactive Materials

- Ti, TiN, Ta, TaN, W, TiW
 - Etch rate depends on temperature.
 - Etch rates from 0 to 300 Å/min



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Low Attack Materials

Material	Typical Selectivity
Thermal SiO ₂	1000:1
Low Temperature SiO ₂	1000:1
Si ₃ N ₄	100:1
Gold	Low Amount of Attack Under Certain Conditions
Copper	
SiC	

Non Reactive Materials

Metals	Compounds	Polymers and Organics
Al	PZT	Photo Resists
Ni	MgO	PDMS
Cr	ZnO	C ₄ F ₈
Pt	AlN	Silica Glass
Ga	GaAs	PVC (Dicing Tape)
C	HfO	PP
	AlO	PEN
		PET
		ETFE
		Acrylic

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Application Areas

Sample of Applications

- RF Switches
- Projection Displays
- Reflective Displays
- Optical Attenuators
- Airborne Virus Detectors
- Chemical Sensors
- Pumps
- Oscillators
- IR Sensors
- Disk Drives
- Heaters/Micro Hot Plates

Sample Structures

- Switches
- Cantilevers
- Bridges
- Mirrors
- Membranes
- Channels
- Thermal Isolation Cavities

Poly-Silicon Micromachining



- High selectivity, excellent reach, fast etch times make XeF_2 an ideal solution for surface micromachining.
- Example of Common Commercial Application:
Undercut = 30 μm to 100 μm , etch rate \sim 3 $\mu\text{m}/\text{min}$

Etch Hole $\sim 1\mu\text{m}^2$

Device: Material Sandwich



Sacrificial Silicon: ~400nm Thick

Oxide Etch Stop: ~200nm thick

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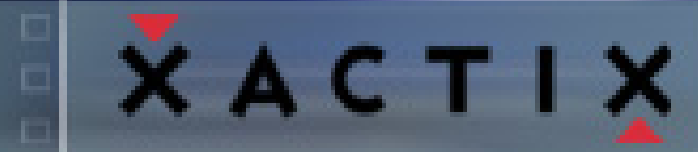
Device: Material Sandwich



Sacrificial Silicon: $\sim 400\text{nm}$ Thick

Oxide Etch Stop: $\sim 200\text{nm}$ thick

Examples Using XeF₂



ADI Optical iMEMS Mirror

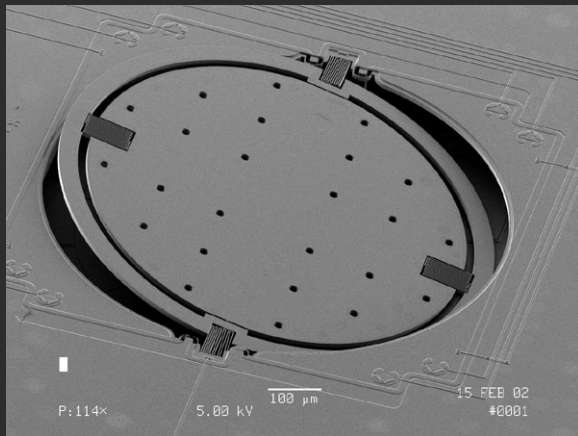


Image Courtesy of Analog Devices

Undercut Photoresist Using CMOS Poly 2

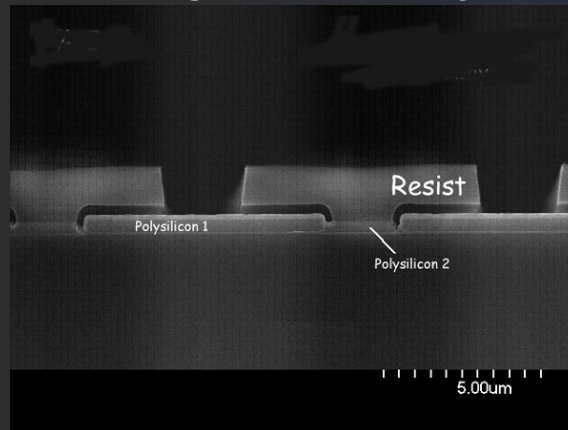


Image courtesy of XACTIX Customer

Cooling Tubes Etched at the Bottom of DRIE Trench

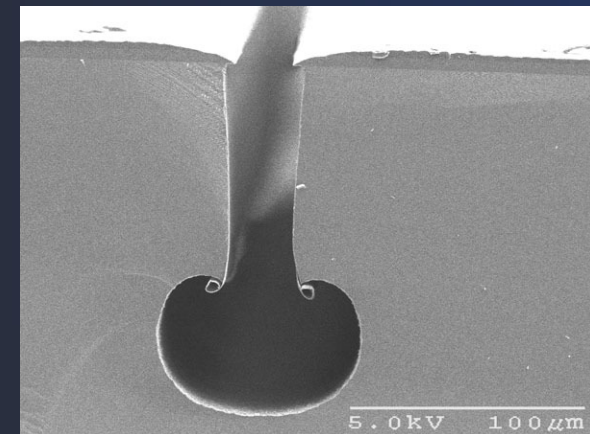


Image Courtesy of Carnegie Mellon University MEMS Laboratory

Examples Using XeF₂



QMT iMod Display



Image Courtesy of Qualcomm

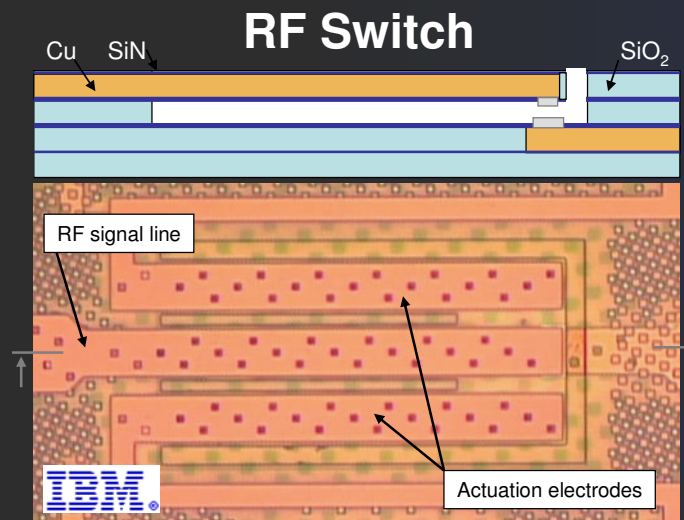


Image Courtesy of IBM

Poly SiC Membrane

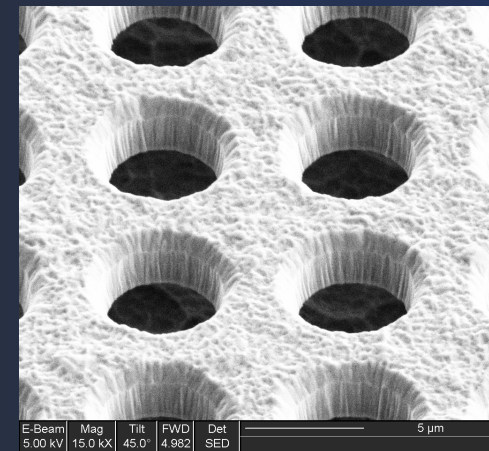
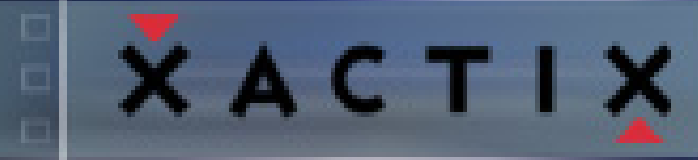


Image Courtesy of Dr. Roger Howe, Stanford University

Examples Using XeF₂



PZT Pump

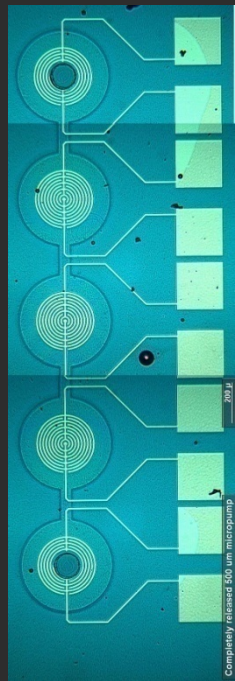


Image courtesy of The Pennsylvania State University and Northrop Grumman Corporation

Nano-Cantilever

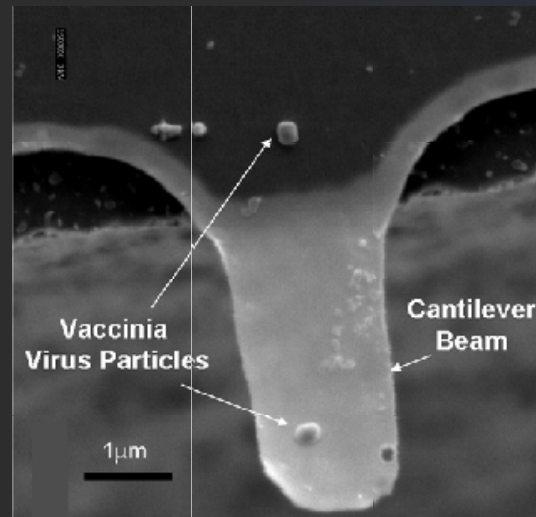


Image Courtesy of Dr. Rashid Bashir, Purdue University

Covered Trenches

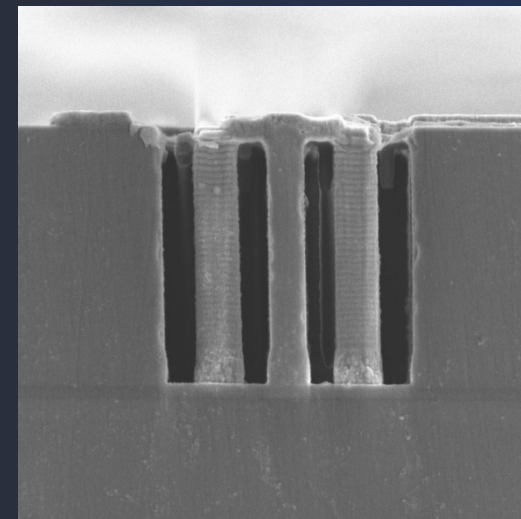


Image Courtesy of Dr. Oliver Brand, Georgia Tech

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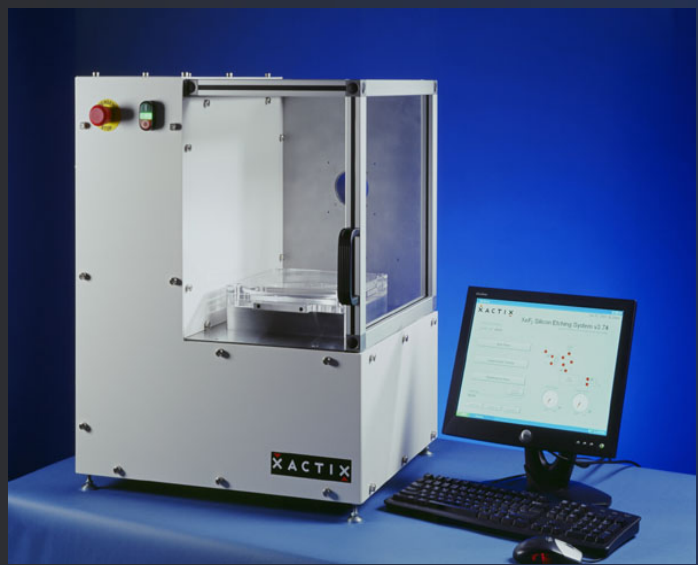
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Xetch X3: R&D Through Low Volume Production



Xetch e1: Low Cost R&D



STS XeF₂ Module

Volume Production

Leading Xenon
Difluoride System for
MEMS Volume
Production

- Module for STS Wafer Handling Platform
- Multiple Wafer Handling Options
- 6" and 8" Capabilities
- Combine with Other STS Tools in Cluster Configuration.



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Conclusion

- XeF₂ Isotropic Etch: Si, Mo, Ge
- Possible to etch W, TiW, Ti, Ta, TiN and TaN
- Very Low or No Attack on Most Other Materials
- Wide Range of Application Areas and Device Structures
- Wide Range of Equipment Options for R&D and Volume Production
- Unequaled Experience and R&D Using XeF₂